

### REMARKS

The above election along with the following remarks are being submitted as a full and complete response to Examiner's further Election / Restriction Requirement on April 29, 2003. Applicants hereby submit that no new matter is being introduced into the application through the submission of this response.

The Examiner indicated that claims 3 and 4 had been incorrectly included in Group I, in that even though claim 3 is dependent on claim 2, the structure of the dielectric layer and the pixel electrode recited in claim 3 appeared inconsistent with the species recited in claim 2. Consequently, the Examiner considered claims 3 and 4 to represent a separate species as illustrated in Figure 14.

Claims 1, 2, 5, 9-11, 22, that are drawn to a liquid crystal display device are provisionally elected for continuous prosecution, while remaining claims 3-4, 6-8, and 12-21, are provisionally withdrawn from further prosecution without prejudice or disclaimer. However, Applicants respectfully traverse this further election/restriction.

As noted above, claims 2-4 are being amended as set forth above and in the attached marked-up presentation of the claim amendments, in order to eliminate the inconsistency indicated by the Examiner.

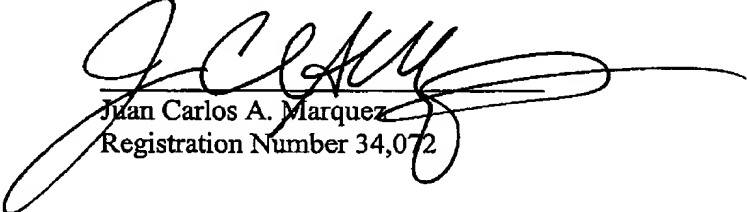
Further, Applicants will contend that the pixel structure of the LCD defined by claim 3 (as shown in Fig. 14) is **in common with** that shown in Fig. 11 (the elected species) in view of (1) the protective film *PSV* being excluded from the charge-holding capacitance section *Cstg* and (2) the contact hole *CNS* being formed inside the semiconductor layer *AS* formed thereat as shown in Fig. 10. As such, claims 3 and 4 fall within the same species as that of claim 2. In view of the above amendments and remarks, the Examiner is respectfully requested to reconsider the propriety of the above-discussed supplemental election/restriction, to review the substance of claims 1-5, 9-11, 22 on the merits, and to indicate the allowability of the claims.

As indicated in the previous response to the election of species requirement, it is understood that if a generic claim is found allowable, the non-elected species claims will be drawn back into the case, and allowed with the other allowed claims in the case. However, Applicants hereby reserve the right to file a divisional application on the non-elected claims.

Substantive consideration of the elected claims is respectfully solicited. Should there be any outstanding issues requiring discussion that would further the prosecution and allowance of the above-captioned application, the Examiner is invited to contact the Applicants' undersigned representative at the address and phone number indicated below.

Respectfully submitted,

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**May 29, 2003**

SPF/JCM/JT

**Marked-up Version of Amended Claims**

2. A liquid crystal display device comprising:

a first insulating substrate and a second insulating substrate disposed to be opposite to the first insulating substrate;

a liquid crystal layer being interposed between the first insulating substrate and the second insulating substrate;

a plurality of gate wiring lines, each of which is formed on the first insulating substrate and transmits a scanning signal;

a gate insulating film being formed on the first insulating substrate and the plurality of gate wiring lines;

a plurality of drain wiring lines, each of which is formed on the gate insulating film and transmits a video signal;

a plurality of semiconductor layers being formed on the gate insulating film and at least under one of the plurality of drain wiring lines;

thin film transistor sections provided for respective pixels, each of the thin film transistor sections [which] has a semiconductor channel layer formed of a part of [the] one of the plurality of semiconductor layers extended at least over a part of one of the plurality of gate wiring lines, a drain electrode formed of a part of the one of the plurality of drain wiring lines situated on the semiconductor channel layer, a source electrode formed on the semiconductor channel layer at an opposite side of the part of the one of the plurality of gate wiring lines to the drain electrode to be spaced from the drain electrode; and

a protective film covering the plurality of drain wiring lines, the source electrodes, and the drain electrodes[;], wherein

each of the respective pixels has a [plurality of] pixel electrode [electrodes,] formed on the protective film and [each of which is] contacted with the source electrode of one of the thin film transistor sections[;] through a first contact hole perforating the protective film and a charge-holding capacitance section [sections,] having [each of

which has] an upper electrode connected to one of the pixel electrode through a second contact hole perforating the protective film and a lower electrodes formed of another of the plurality of the gate wiring line or a material thereof, [wherein,]

a dielectric film being interposed between the lower electrode and the upper electrode of the charges-holding capacitance section [each of the holding capacitance sections] includes at least [has a stacked layer structure formed of] the gate insulating film[and the semiconductor layer], and

the charges-holding capacitance section is provided [each of the Pixel electrodes is contacted] with another [one] of the plurality of semiconductor layers having a planar outline inside which the second [through a] contact hole is located [provided by perforating the protective film] and being contact with the pixel electrode.

3. A liquid crystal display device according to claim 2, wherein the second contact hole perforates the another of the plurality of the semiconductor layer also at the charges-holding capacitance section [the dielectric film interposed between the lower electrode and the upper electrode of the holding capacitance is the gate insulating film], and the pixel electrode contacts with the gate insulating film through the second contact hole [provided by perforating the protective film].
4. A liquid crystal display device according to claim 3, wherein the another of the semiconductor layers is formed around the second contact hole on the gate insulating film.